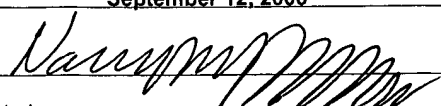





Doc Code: AP.PRE.REQ

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PRE-APPEAL BRIEF REQUEST FOR REVIEW		Docket Number (Optional) ITL.0664US (P12630)	
<p>I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR</p> <p>on <u>September 12, 2006</u></p> <p>Signature <u></u></p> <p>Typed or printed name <u>Nancy Meshkoff</u></p>		Application Number 10/044,614	Filed January 11, 2002
		First Named Inventor Jay P. Hoeflinger	
		Art Unit 2191	Examiner Qamrun Nahar
<p>Applicant requests review of the final rejection in the above-identified application. No amendments are being filed with this request.</p> <p>This request is being filed with a notice of appeal.</p> <p>The review is requested for the reason(s) stated on the attached sheet(s). Note: No more than five (5) pages may be provided.</p> <p>I am the</p> <p><input type="checkbox"/> applicant/inventor.</p> <p><input type="checkbox"/> assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)</p> <p><input checked="" type="checkbox"/> attorney or agent of record. Registration number <u>28,994</u></p> <p><input type="checkbox"/> attorney or agent acting under 37 CFR 1.34. Registration number if acting under 37 CFR 1.34 _____</p> <p> Signature Timothy N. Trop Typed or printed name (713) 468-8880 Telephone number September 12, 2006 Date</p> <p>NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.</p>			

<input type="checkbox"/> *Total of _____ forms are submitted.

This collection of information is required by 35 U.S.C. 132. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11, 1.14 and 41.6. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Jay P. Hoeflinger et al.

Serial No.: 10/044,614

Filed: January 11, 2002

For: Transferring Data Between
Threads in a Multiprocessing
Computer System

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Art Unit: 2191

Examiner: Qamrun Nahar

Docket: ITL.0664US
P12630

Assignee: Intel Corporation

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATEMENT IN SUPPORT OF PRE-APPEAL REQUEST FOR REVIEW

Sir:

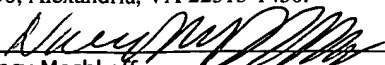
The final rejection posits the following reasoning:

1. Multiple threads have multiple thread-private storages. That is, multiple threads-private storages are multiple temporary storages for the object.
2. Multiple temporary storages are interpreted as a multiple address buffer. For example, if there are two threads, then there would be two address buffer.

While this analysis may be imaginative, there is no basis whatsoever for positing that multiple threads, having their own storage, would necessarily be stored in a two address buffer. They could be stored in a number of buffers, each only having one address. There is no reason whatsoever to presume that in a buffer having two addresses was or would be used under any analysis of the reference.

Date of Deposit: September 12, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

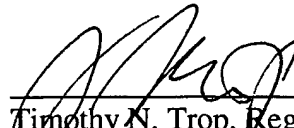

Nancy Meshkoff

Taken to the logical extreme, if there were five threads, there would be a five address buffer, but no such thing even exists so far as the Applicants' attorney is aware of. Thus, the position is logically and technically untenable.

Therefore, reconsideration is requested.

Respectfully submitted,

Date: September 12, 2006



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Attorneys for Intel Corporation